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L3	188	delay$1 and 2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/31 16:36
L4	100041	interpolat$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/31 16:36
L5	78	interpolat$3 and 3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/31 16:36
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L7	6	6 and sigma	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/31 16:48
L8	1932	708/270.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/31 16:48
L9	181	8 and accumulat$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/31 16:49

L10	63	9 and delay$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/31 16:49
L11	15	10 and interpolat$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/31 16:49
L12	15	11 not 7	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/31 16:49
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     Saramaki, T.; Karema, T.; Ritoniemi, T.; Tenhunen, H.;
     Electrotechnical Conference, 1991. Proceedings., 6th Mediterranean

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☐ 1. **VLSI-realizable multiplier-free interpolators for high-order sigma-delta D/A converters**
Saramaki, T.; Karema, T.; Ritoniemi, T.; Tenhunen, H.;
Electrotechnical Conference, 1991. Proceedings., 6th Mediterranean
22-24 May 1991 Page(s):295 - 298 vol.1
AbstractPlus | Full Text: PDF(260 KB) IEEE CNF

☐ 2. **An FPGA-based re-configurable 24-bit 96kHz sigma-delta audio DAC**
Cheung, R.C.C.; Pun, K.P.; Yuen, S.C.L.; Tsoi, K.H.; Leong, P.H.W.;
Field-Programmable Technology (FPT), 2003. Proceedings. 2003 IEEE International Conference on
15-17 Dec. 2003 Page(s):110 - 117
AbstractPlus | Full Text: PDF(568 KB) IEEE CNF

☐ 3. **One bit digital filtering**
Angus, J.A.S.;
Digital Filters: An Enabling Technology (Ref. No. 1998/252), IEE Colloquium on
20 April 1998 Page(s):8/1 - 8/6
AbstractPlus | Full Text: PDF(436 KB) IEE CNF

☐ 4. **Issues in the design of low oversampling ratio single bit sigma-delta modulators**
Hossack, D.M.; Reid, J.; Sewell, J.I.;
Oversampling Techniques and Sigma-Delta Modulation, IEE Colloquium on
30 Mar 1994 Page(s):3/1 - 3/5
AbstractPlus | Full Text: PDF(112 KB) IEE CNF

☐ 5. **Design and implementation of sigma-delta D/A converters with optimized loop filters**
Horbach, U.; Lang, M.;
Circuits and Systems, 1991., IEEE International Sympoisum on
11-14 June 1991 Page(s):1633 - 1636 vol.3
AbstractPlus | Full Text: PDF(272 KB) IEEE CNF

☐ 6. **Use of noise shaping for rate conversion in oversampled video signals**
Ryan, I.; McCarthy, O.;
Circuits and Systems, 2003. ISCAS '03. Proceedings of the 2003 International Symposium on
Volume 4, 25-28 May 2003 Page(s):IV-53 - IV-56 vol.4
AbstractPlus | Full Text: PDF(476 KB) IEEE CNF

☐ 7. **A four-channel digital signal processor in 1.2-μm CMOS with on-chip D/A and A/D conversion serving four speech channels in a new-generation subscriber line circuit**
Haspeslagh, D.; Sevenhans, J.; Delarbre, A.; Kiss, L.; Moerman, E.;
Solid-State Circuits, IEEE Journal of
Volume 26, Issue 7, July 1991 Page(s):1038 - 1046
AbstractPlus | Full Text: PDF(584 KB) IEEE JNL

☐ 8. **High performance signal acquisition systems for sonar applications**
Curtis, T.E.; Webb, A.B.;
Civil Applications of Sonar Systems, IEE Colloquium on
4 Feb 1991 Page(s):2/1 - 2/5
AbstractPlus | Full Text: PDF(212 KB) IEE CNF

☐ 9. **A CMOS oversampling D/A converter with a current-mode semidigital reconstruction filter**
Su, D.K.; Wooley, B.A.;
Solid-State Circuits, IEEE Journal of
Volume 28, Issue 12, Dec. 1993 Page(s):1224 - 1233
AbstractPlus | Full Text: PDF(884 KB) IEEE JNL

10. **Stable single-bit noise-shaping quantizer based on data coding into optimized binary vectors**
Yagyu, M.; Nishihara, A.;
Circuits and Systems, 2002. ISCAS 2002. IEEE International Symposium on
Volume 2, 26-29 May 2002 Page(s):II-384 - II-387 vol.2
AbstractPlus | Full Text: PDF(502 KB)   IEEE CNF

11. **A CMOS oversampling D/A converter with a current-mode semi-digital reconstruction filter**
Su, D.K.; Wooley, B.A.;
Solid-State Circuits Conference, 1993. Digest of Technical Papers. 40th ISSCC., 1993 IEEE International
24-26 Feb. 1993 Page(s):230 - 231
AbstractPlus | Full Text: PDF(172 KB)   IEEE CNF

12. **A novel interpolator architecture for $\Sigma\Delta$ DACs**
Francesconi, F.; Lazzari, G.; Liberali, V.; Maloberti, F.; Torelli, G.;
Design Automation, 1993, with the European Event in ASIC Design. Proceedings. [4th] European
Conference on
22-25 Feb. 1993 Page(s):249 - 253
AbstractPlus | Full Text: PDF(248 KB)   IEEE CNF

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